



汕头华汕电子器件有限公司

INSULATED TYPE TRIAC

**HBT137F-600**

对应国外型号  
BT137F-600

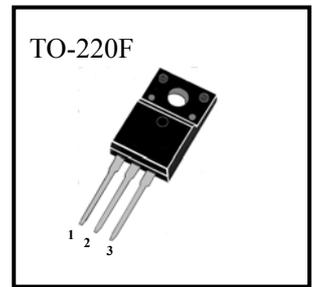
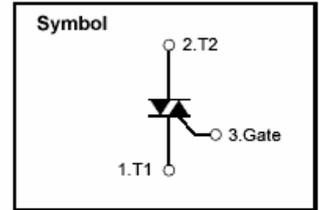
■ 主要用途

绝缘型双向可控硅, 用于交流开关、风扇控制、温度控制、照明控制等

■ 极限值 (T<sub>a</sub>=25°C)

T <sub>stg</sub> ——贮存温度	.....	-40~150°C
T <sub>j</sub> ——结温	.....	-40~125°C
P <sub>GM</sub> ——峰值门极功耗	.....	5W
V <sub>DRM</sub> ——重复峰值断态电压	.....	600V
I <sub>T(RMS)</sub> ——RMS 通态电流 (T <sub>c</sub> =76°C)	.....	8A
V <sub>GM</sub> ——峰值门极电压	.....	10V
I <sub>GM</sub> ——峰值门极电流	.....	2.0A
I <sub>TSM</sub> ——浪涌通态电流(一个周期,50/60Hz,峰值,不重复)	.....	70/77A
V <sub>ISO</sub> ——绝缘击穿电压(RMS, 交流 1 分钟)	.....	1500V

■ 外形图及引脚排列



■ 电参数 (T<sub>a</sub>=25°C)

参数符号	符号说明	最小值	典型值	最大值	单位	测试条件
I <sub>DRM</sub>	重复峰值断态电流			1.0	mA	V <sub>D</sub> =V <sub>DRM</sub> , 单相, 半波, T <sub>J</sub> =125°C
V <sub>TM</sub>	峰值通态电压			1.6	V	I <sub>T</sub> =10A, 快速测量
I <sub>+GT1</sub>	门极触发电流 (I)			25	mA	V <sub>D</sub> =6V, R <sub>L</sub> =10 ohm
I <sub>-GT1</sub>	门极触发电流 (II)			25	mA	V <sub>D</sub> =6V, R <sub>L</sub> =10 ohm
I <sub>-GT3</sub>	门极触发电流 (III)			25	mA	V <sub>D</sub> =6V, R <sub>L</sub> =10 ohm
V <sub>+GT1</sub>	门极触发电压 (I)			1.5	V	V <sub>D</sub> =6V, R <sub>L</sub> =10 ohm
V <sub>-GT1</sub>	门极触发电压 (II)			1.5	V	V <sub>D</sub> =6V, R <sub>L</sub> =10 ohm
V <sub>-GT3</sub>	门极触发电压 (III)			1.5	V	V <sub>D</sub> =6V, R <sub>L</sub> =10 ohm
V <sub>GD</sub>	不触发门极电压	0.2			V	T <sub>J</sub> =125°C, V <sub>D</sub> =1/2V <sub>DRM</sub>
(dv/dt) <sub>c</sub>	断态电压临界上升率	5.0			V/μS	T <sub>J</sub> =125°C, V <sub>D</sub> =2/3V <sub>DRM</sub>
R <sub>th(j-c)</sub>	热阻			3.8	°C/W	(di/dt) <sub>c</sub> =-3.0A/ms 结到外壳
I <sub>H</sub>	维持电流		10		mA	



■ 特性曲线

Fig 1. Gate Characteristics

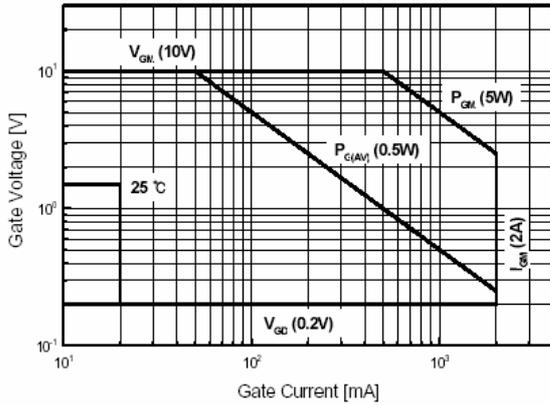


Fig 2. On-State Voltage

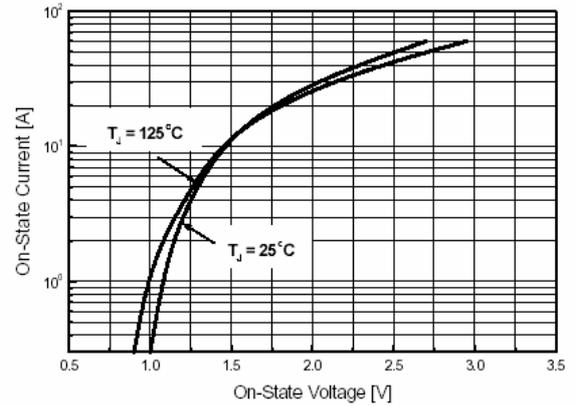


Fig 3. On State Current vs. Maximum Power Dissipation

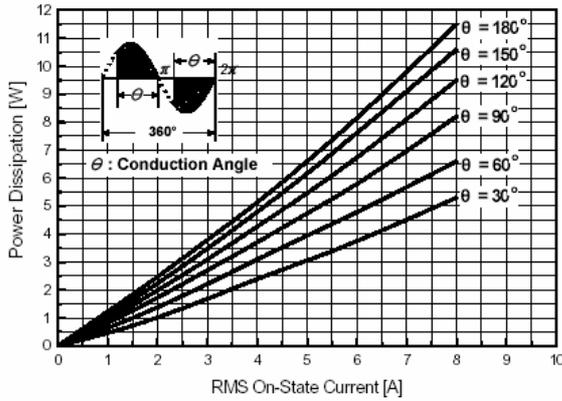


Fig 4. On State Current vs. Allowable Case Temperature

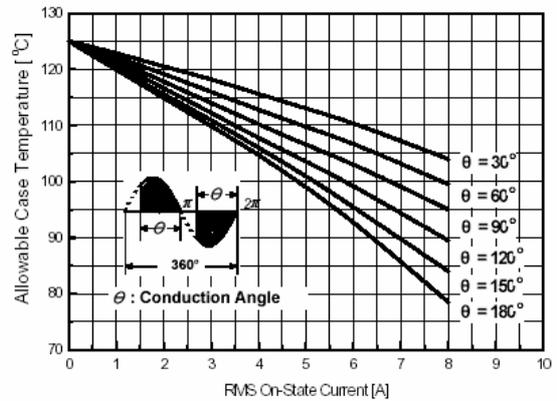


Fig 5. Surge On-State Current Rating (Non-Repetitive)

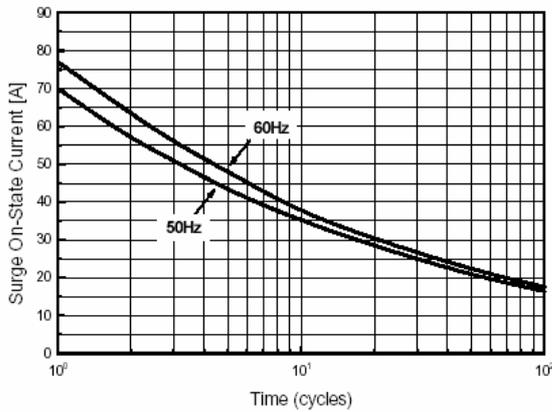
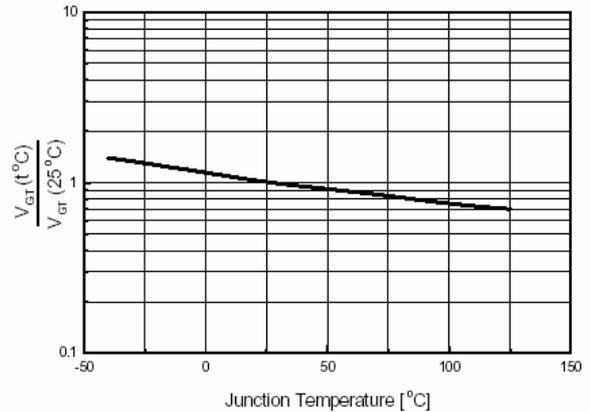


Fig 6. Gate Trigger Voltage vs. Junction Temperature





■ 特性曲线

Fig 7. Gate Trigger Current vs. Junction Temperature

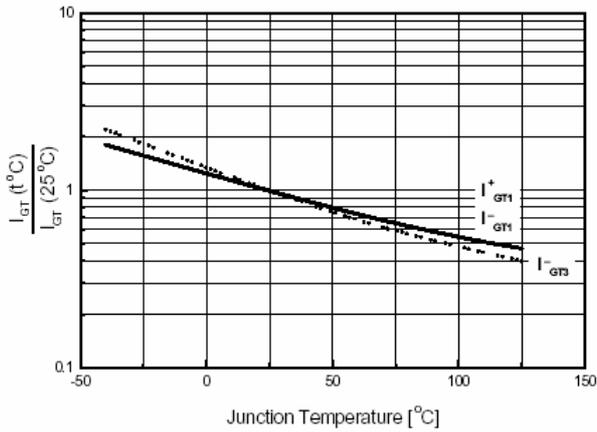


Fig 8. Transient Thermal Impedance

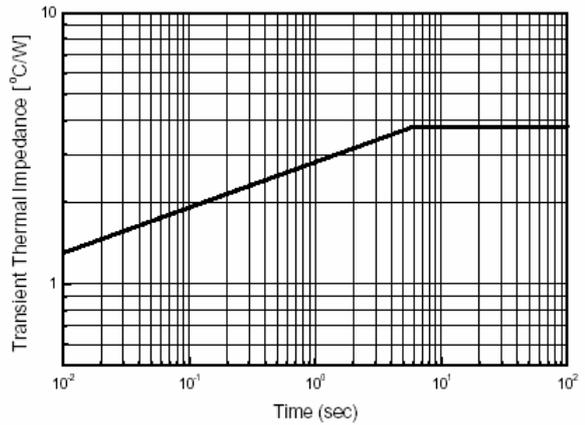
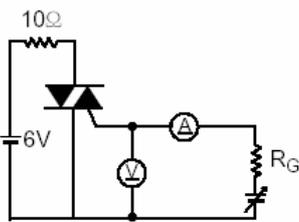
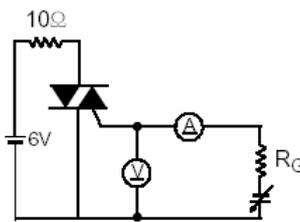


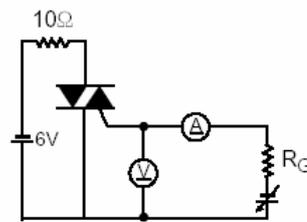
Fig 9. Gate Trigger Characteristics Test Circuit



Test Procedure I



Test Procedure II



Test Procedure III