



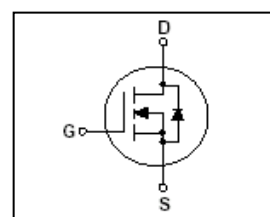
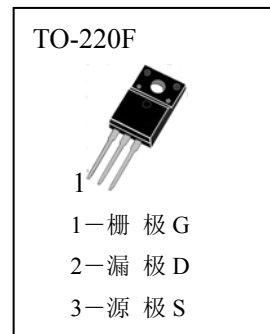
主要用途

高速开关应用。

极限值 (T_a=25°C)

T _{stg}	贮存温度	-55~150°C
T _j	结温	150°C
V _{DSS}	漏极—源极电压	650V
V _{GS}	栅极—源极电压	±30V
I _D	漏极电流 (T _c =25°C)	1.8A
I _{DM}	漏极电流 (脉冲) (注 1)	7.2A
P _D	耗散功率 (T _c =25°C)	22.8W

外形图及引脚排列



电参数 (T_a=25°C)

参数符号	符号说明	最小值	典型值	最大值	单位	测试条件
BV _{DSS}	漏—源极击穿电压	650			V	I _D =250μA, V _{GS} =0V
I _{DSS}	零栅压漏极电流			1	μA	V _{DS} =650V, V _{GS} =0
I _{GSS}	栅极泄漏电流			±100	nA	V _{GS} =±30V, V _{DS} =0V
V _{GS(th)}	栅—源极开启电压	2.5		4.5	V	V _{DS} =V _{GS} , I _D =250μA
R _{DS(on)}	漏—源极导通电阻		4.5	6.0	Ω	V _{GS} =10V, I _D =0.9A
C _{iss}	输入电容		260	340	pF	V _{DS} =25V, V _{GS} =0, f=1MHz
C _{oss}	输出电容		25	33	pF	
C _{rss}	反向传输电容		5.5	7	pF	
t _{d(on)}	导通延迟时间		15	30	nS	V _{DS} =325V, I _D =1.8A (峰值) R _G =25Ω (注 2)
t _r	上升时间		40	80	nS	
t _{d(off)}	断开延迟时间		40	80	nS	
t _f	下降时间		30	60	nS	V _{DS} =520V V _{GS} =10V I _D =1.8A (注 2)
Q _g	栅极总电荷		8.5	11	nC	
Q _{gs}	栅极—源极电荷		2.0		nC	
Q _{gd}	栅极—漏极电荷		4.0		nC	
I _s	源极—漏极二极管正向电流			1.8	A	
V _{SD}	源极—漏极二极管导通电压			1.4	V	I _s =1.8A, V _{GS} =0
R _{th(j-c)}	热阻			5.5	°C/W	结到外壳

*注 1: 漏极电流受最大结温限制。

*注 2: 脉冲测试, 宽度≤300μS, 占空比≤2%



典型特性曲线

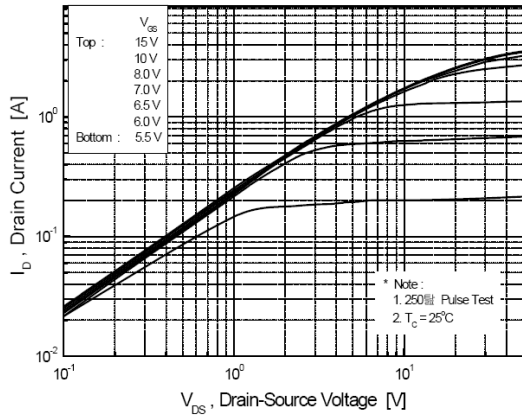


Figure 1. On Region Characteristics

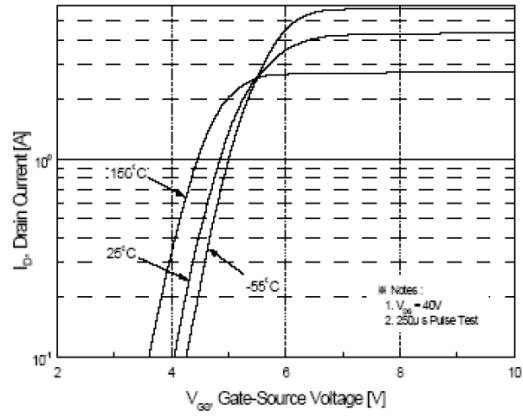


Figure 2. Transfer Characteristics

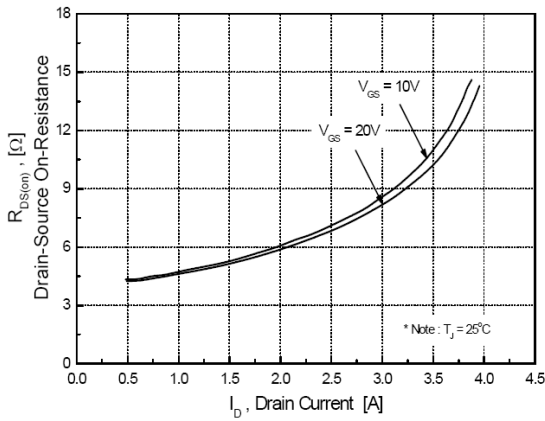


Figure 3. On Resistance Variation vs Drain Current and Gate Voltage

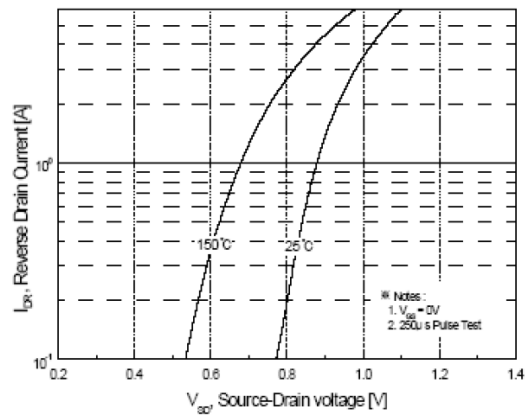


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

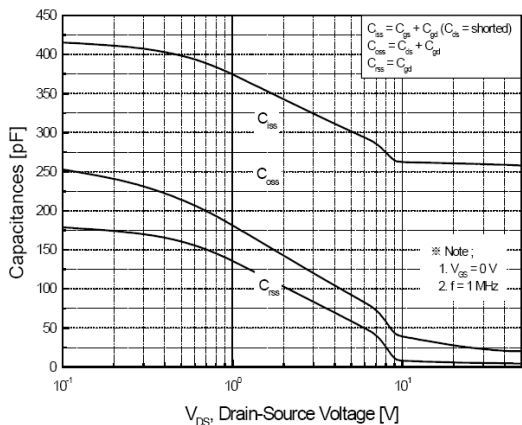


Figure 5. Capacitance Characteristics

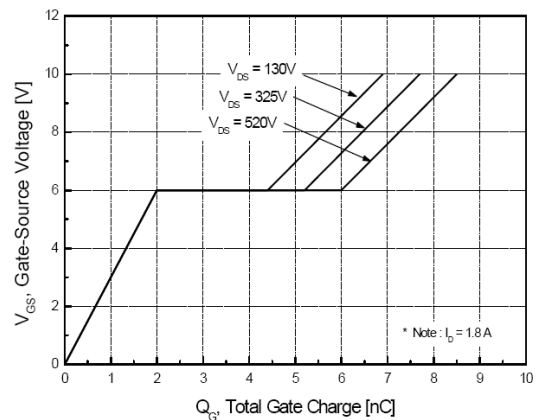


Figure 6. Gate Charge Characteristics



■ 典型特性曲线

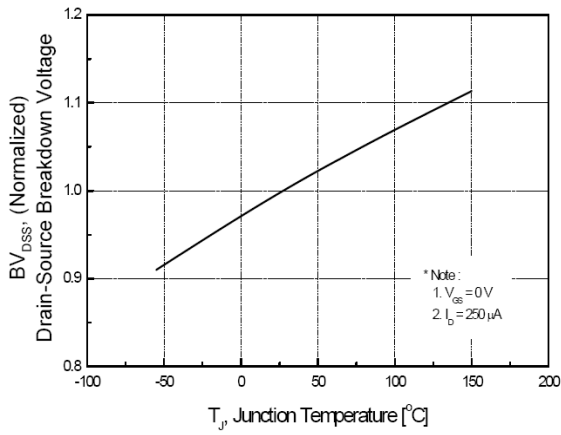


Figure 7. Breakdown Voltage Variation vs Temperature

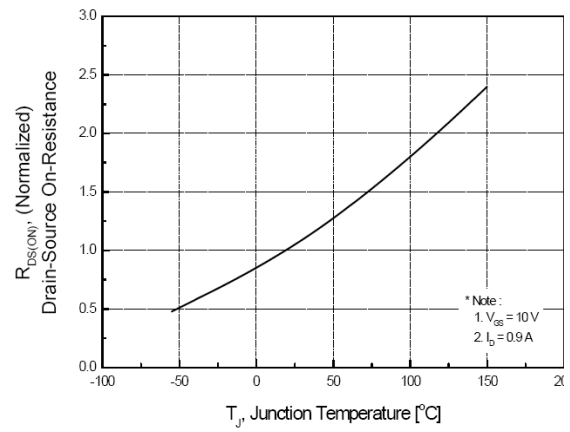


Figure 8. On-Resistance Variation vs Temperature

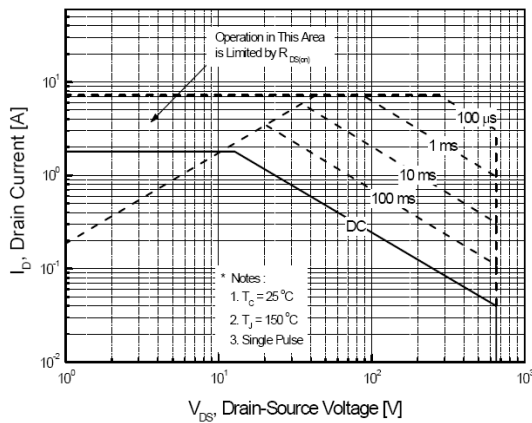


Figure 9. Maximum Safe Operating Area

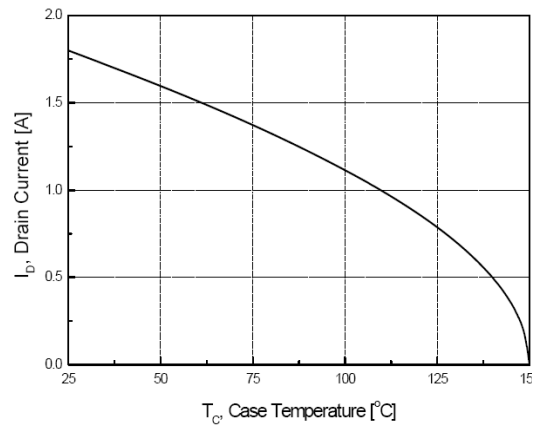


Figure 10. Maximum Drain Current vs Case Temperature

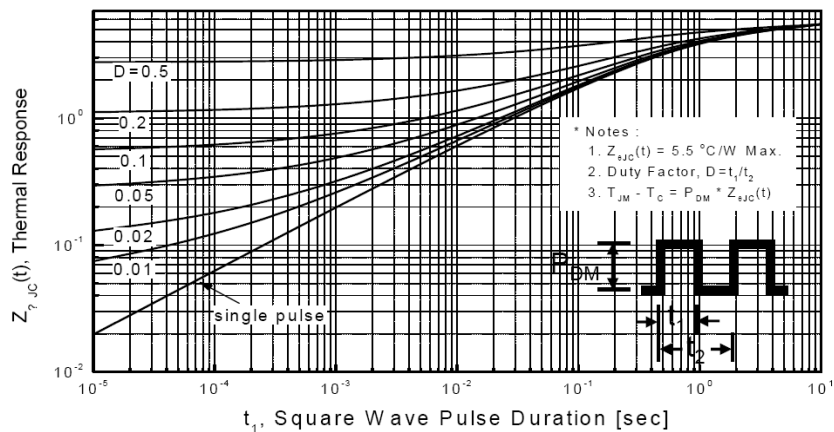


Figure 11. Transient Thermal Response Curve



■ 典型特性曲线

Fig 12. Gate Charge Test Circuit & Waveform

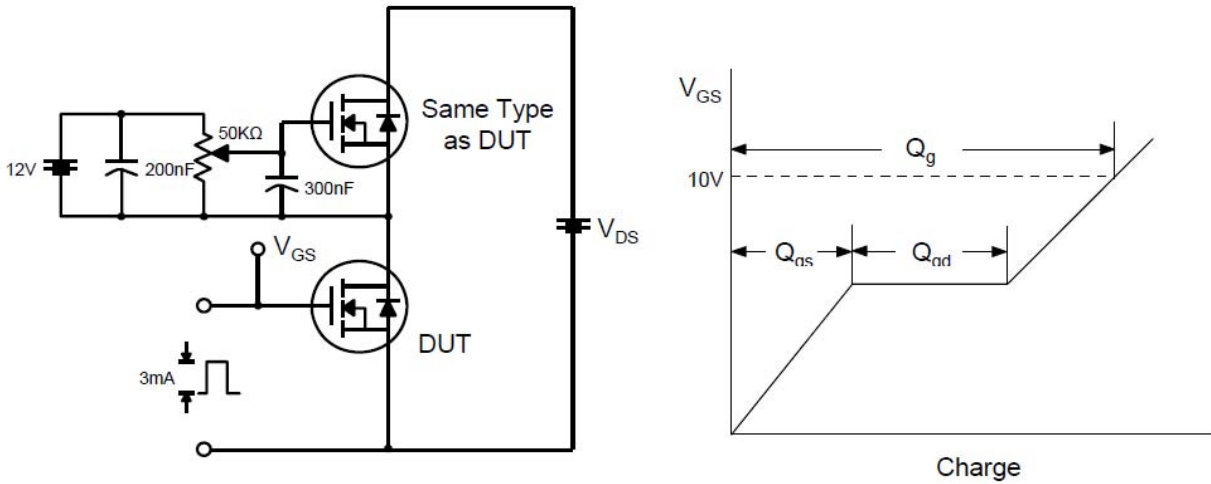


Fig 13. Resistive Switching Test Circuit & Waveforms

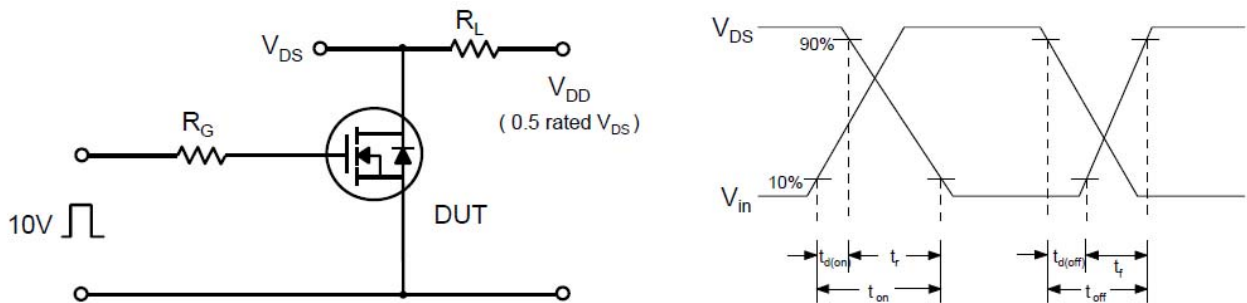
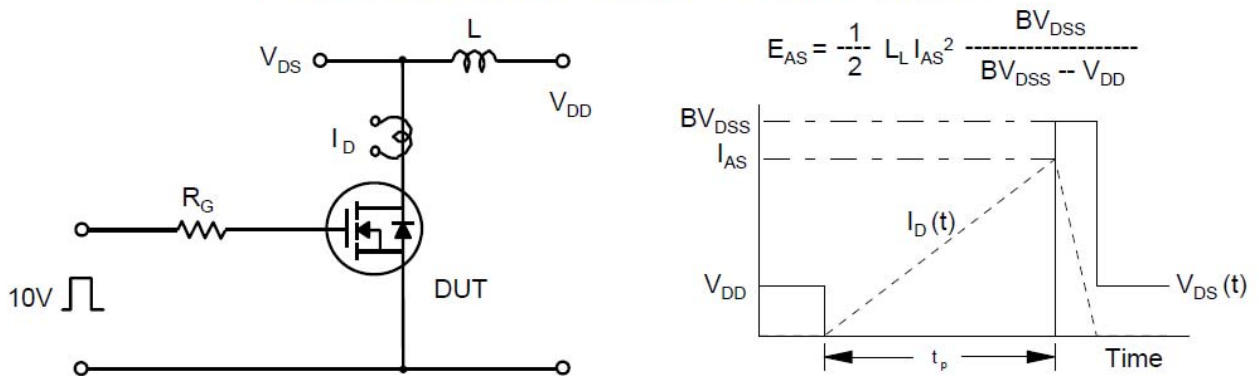


Fig 14. Unclamped Inductive Switching Test Circuit & Waveforms





■ 典型特性曲线

Peak Diode Recovery dv/dt Test Circuit & Waveforms

